

**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**
(Not for submission under 37 CFR 1.99)

Application Number	10619781
Filing Date	2003-07-14
First Named Inventor	Naga Bhushan
Art Unit	2617
Examiner Name	Michael T. Vu
Attorney Docket Number	030168U1

U.S. PATENTS

Examiner Initial*	Cite No	Patent Number	Kind Code ¹	Issue Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines where Relevant Passages or Relevant Figures Appear
/M.V./	1	5774496		1998-06-30	BUTLER et al	
/M.V./	2	5802106		1998-09-01	PACKER, Robert L.	
/M.V./	3	5914950		1999-06-22	TIEDEMANN, Jr., et al	
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/M.V./	6	6314101		2001-10-06	REZAIIIFAR et al.	
/M.V./	7	6671512		2008-02-20	LAAKSO, Janne	
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/M.V./	9	6907005		2005-06-14	DAHLMAN et al.	
/M.V./	10	6931569		2005-08-16	FONG et al	
/M.V./	11	6999425		2006-02-14	CHENG et al	
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	1	EP1024623						<input type="checkbox"/>
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	3	WO0201743						<input type="checkbox"/>
	4	WO0209342						<input type="checkbox"/>

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	5	WO0343218						<input type="checkbox"/>
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/M.V./	1	ATTAR et al., "A Reverse Link Outer-Loop Power Control Algorithm for CDMA2000 1xEV Systems," IEEE International Conference on Communications, 2002. ICC 2002. Volume 1, 28 April - 2 May 2002. pgs.573-578	<input type="checkbox"/>
/M.V./	2	CHAKRAVARTY et al., "An Algorithm for Reverse Traffic Channel Rate Control for CDMA2000 High Rate Packet Data Systems," IEEE Global Telecommunications Conference, 2001. GLOBECOM '01. Volume 6, 25-29 November 2001, pgs.3733-3737	<input type="checkbox"/>
/M.V./	3	ESTEVEZ, E., "On the Reverse Link Capacity of CDMA2000 High Rate Packet Data Systems," IEEE International Conference on Communications, 2002. Volume 3, 28 April - 2 May 2002, pgs.1823-1828	<input type="checkbox"/>
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